

[illegible]

The schematic diagram illustrates the ESD protection circuit for the TMS320C6701. It features two ESD protection ICs, U3 and U4, connected to the TMS320C6701 pins. U3 is connected to pins 1 (TMS\_SWDIO), 3 (TCK\_SWCLK), 5 (T\_RESET), 6 (T\_VCP\_RX), 7 (T\_TDI\_VCP\_TX), 8 (TRST\_VCP\_RTS), and 9 (T\_DDO\_SWO). U4 is connected to pins 1 (TMS\_SWDIO), 3 (T\_DDO\_SWO), 4 (T\_RESET), 5 (TCK\_SWCLK), 6 (T\_TDI\_VCP\_TX), and 7 (TRST\_VCP\_RTS). The circuit is powered by TVDD and connected to GND.

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Pin 1-5 connection diagram for CN2:

- Pin 1: PWR\_B0010
- Pin 2: SWD\_SDA (connected to 3V3\_PYLK)
- Pin 3: (unlabeled)
- Pin 4: SWD\_SCK (connected to GND)
- Pin 5: (unlabeled)

1  
2  
3  
JP1

TVDD\* R34 OR TVDD

3V3\_USB\_PYLK 3V3\_USB\_PYLK 5V\_USB\_PYLK

Title: PY32F403C1xT_LINK.SchDoc		
Project: PY32F410RxT6_START.PrjPcb		
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## VDD\_Selection

## Debug Connector

The diagram illustrates the Debug Connector (Header 5) with the following connections:

- Pin 1: PA13 (connected to R51 and TMS SWDIO)
- Pin 2: PA14 (connected to R52 and TCK SWCLK)
- Pin 3: PD11 (connected to R54 and T\_RESET)
- Pin 4: TVDD
- Pin 5: GND

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# OSC

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## Led

The diagram shows a green LED connected to the microcontroller pin PB2. The LED's anode is connected to PB2 via a 680R resistor, and its cathode is connected to the TVDD pin. The current through the LED is 10mA.

## USART\_Selection

Diagram illustrating the USART Selection pinout:

Pin	Signal
1	PA9
2	PA10
3	PA2
4	PA3
5	T VCP_RX
6	T TDI VCP_TX

JP3

Boot\_Selection

TVDD

1

2

3

JP4

R55 10K BOOT0

GND